

COMPUTER PROCESSING SYSTEM AND PROCESSING METHOD PERFORMED IN COMPUTER

Patent number: JP2002342165
Publication date: 2002-11-29
Inventor: SUZUOKI MASAKAZU; YAMAZAKI TAKESHI
Applicant: SONY COMP ENTERTAINMENT INC
Classification:
 - international: G06F12/14; G06F15/16
 - european: G06F12/14D1
Application number: JP20020079358 20020320
Priority number(s): US20010816020 20010322

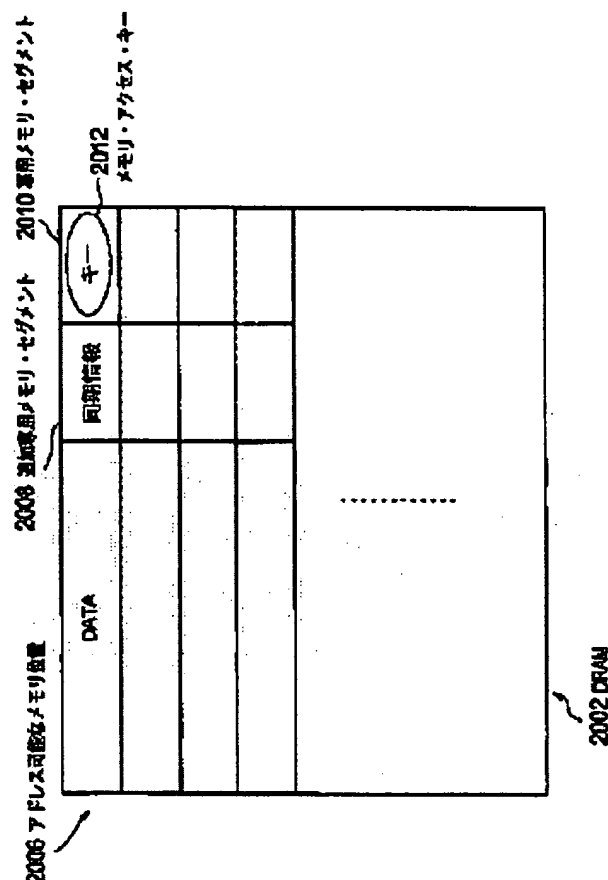
Also published as:

WO02077826 (A1)
 EP1370948 (A1)
 US6526491 (B2)
 US2002138701 (A1)
 JP2003303134 (A)

Report a data error here

Abstract of JP2002342165

PROBLEM TO BE SOLVED: To provide a computer architecture and programming model for high speed processing over broadband networks. **SOLUTION:** The architecture employs a consistent modular structure, a common computing module and uniform software cells. The common computing module includes a control processor, a plurality of processing units, a plurality of local memories from which the processing units process programs, a direct memory access, controller and a shared main memory. A synchronized system and method for the coordinated reading and writing of data to and from the shared main memory by the processing units also are provided.



Data supplied from the esp@cenet database - Worldwide